

**Office of the Dean Research and Consultancy
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103**

Project on: "Design and Development of WBG Device Based High Current Converters for Industry Applications"

[Sponsoring Authority: MeITy, Govt. of India]

**Department of Electrical Engineering,
Indian Institute of Engineering Science & Technology (IEST), Shibpur
Howrah-711 103**

**Ref: Advt. No. EE 1681, published in the "Statesman (All Editions)" & "Dainik Statesman", dated
25.06.2021**

[University Project Code: DRC/MEITY-NAMPET-III(WBG)/EE/MS/006/20-21]

Interested candidates are requested to appear for an online interview(s), for the following post in the Department of Electrical Engineering, **Indian Institute of Engineering Science & Technology (IEST), Shibpur, Howrah-711103.**

Name of the post: Project Associate

No of post(s)-One (01)

Essential Qualification(s): 1. M.E. / M.Tech. or equivalent degree in Electrical Engineering from a recognized Indian University/Institute.
2. At least 2 years' experience (research/professional) in relevant field of Electrical engineering.

Desirable Qualification(s): 1. Knowledge of simulation using FEM and power electronics simulation software.
3. At least one published papers in SCI/SCIE/SCOPUS listed or other reputed indexed journal in relevant field.
4. First Class in Bachelor's and Master's Degree.
5. Experience of working on Power Electronic hardware for at least 3 years.

Fellowship: Rs. 35,000/- + HRA as per GoI rules

Age Limit: Preferably below 35 years (Upper age limit is relaxable upto 5 years for SC/ST/OBC/Woman and physically handicapped candidates).

Duration: 32 months or till the termination of the project whichever is earlier.

Interested eligible candidates should mail soft copies of the application letter in plain paper, recent bio-data, mark sheets and certificates. All documents should be self-attested. Physical documents will be verified at the time of joining. The selection will be canceled if any discrepancies are found in the documents at the time of physical verification.

Venue and Date of the interview: ONLINE. Information about the date and the link of the interview will be sent to the eligible candidates only by e-mail.

Note:

1. Soft copies of the application letter, bio-data, mark sheets and certificates should be sent through e-mail in advance by 15 days from the date of newspaper publication to: Prof. Mainak Sengupta, Chief Investigator (E-mail id: msg@ee.iests.ac.in, msg.ee@faculty.iests.ac.in).
2. All applications must mention a valid e-mail id and phone number for communicating date of interview.
3. Short listing may be done before the interview.

Dean (R & C)

(W. Code DRC-005/21-22)

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Interested candidates are requested to appear for an online interview(s), for the following post in the Department of Electrical Engineering, **Indian Institute of Engineering Science & Technology (IEST), Shibpur, Howrah-711103.**

Name of the post: Senior Project Associate

No of post(s)–One (01)

Essential Qualification(s): 1. M.E. / M.Tech. or equivalent degree in Electrical Engineering from a recognized Indian University/Institute.
2. At least 4 years' experience (research/professional) in Electrical Engineering from a recognized Indian University/Institute/ research organization.
or

Doctoral Degree in relevant field of electrical engineering

Desirable Qualification(s): 1. Knowledge of simulation using FEM and power electronics simulation software.
3. At least two published papers in SCI/SCIE/SCOPUS listed or other reputed indexed journal in relevant field.
4. First Class in Bachelor's and Master's Degree.
5. Experience of working on Power Electronic hardware for at least 5 years.
6. Knowledge of PCB design.
7. Good knowledge of Power Semiconductor Devices.

Fellowship: Rs.35,000/- + HRA as per GoI rules

Age Limit: 40 years (relaxable upto 5 years for SC/ST/OBC/Woman and physically handicapped candidates).

Duration: 32 months or till the termination of the project whichever is earlier.

Interested eligible candidates should mail soft copies of the application letter in plain paper, recent bio-data, mark sheets and certificates. All documents should be self-attested. Physical documents will be verified at the time of joining. The selection will be canceled if any discrepancies are found in the documents at the time of physical verification.

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